

WHAT IS CLAIMED IS:

1. A liquid crystal display (LCD) panel comprising:
a first substrate provided with a plurality of gate and data lines, the gate lines being arranged to cross the data lines to define a plurality of pixel regions in a matrix arrangement;
a second substrate provided with a black matrix layer to shield portions other than the pixel regions from light; and
a liquid crystal layer injected between the first and second substrates, wherein the pixel regions in a peripheral portion of the matrix arrangement has an aperture ratio lower than that of the pixel regions in other portions of the matrix arrangement.
2. The liquid crystal display panel of claim 1, wherein a first gate line among the gate lines has a greater width than widths of the other gate lines so as to allow the pixel regions in the peripheral portion to obtain an aperture ratio lower than that of the pixel regions in the other portions.
3. The liquid crystal display panel of claim 1, wherein a first data line or a last data line among the data lines has a greater width than widths of the other data lines so as to allow the pixel regions in the peripheral portion to obtain an aperture ratio lower than that of the pixel regions in the other portions.

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4. The liquid crystal display panel of claim 1, wherein the black matrix layer corresponding to at least one of a first gate line, a first data line and a last data line has a greater width than widths of the black matrix layer corresponding to the other portions so as to allow the pixel regions in the peripheral portion to obtain an aperture ratio lower than that of the pixel regions in the other portions.

5. The liquid crystal display panel of claim 1, further comprising a light-shielding pattern in the pixel regions at the peripheral portion so as to allow the pixel regions at the peripheral portion to obtain an aperture ratio lower than that of the pixel regions at the other portions.

6. The liquid crystal display panel of claim 1, wherein an aperture ratio of the pixel regions in the peripheral portion is about 10~15% lower than aperture ratios of the pixel regions at the other portions.

7. An liquid crystal display panel comprising:
a plurality of gate lines arranged in one direction at constant intervals;

a plurality of data lines arranged at constant intervals to be substantially perpendicular to the gate lines to define a plurality of pixel regions in a matrix arrangement; and

a plurality of pixel electrodes, wherein one pixel electrode is in each pixel region, wherein the pixel electrodes at a peripheral portion of the matrix arrangement are narrower than the pixel electrodes at other portions of the matrix arrangement.

8. The liquid crystal display panel of claim 7, wherein a gate line that does not drive the pixel electrode among the gate lines has a greater width than widths of other gate lines.

9. The liquid crystal display panel of claim 7, wherein at least one of a first data line and a last data line among the data lines has a greater width than widths of other data lines.

10. The liquid crystal display panel of claim 7, wherein area of the pixel electrodes adjacent to at least one of a first data line and a last data line is less than areas of the pixel electrodes at the other portions.

11. An liquid crystal display panel, comprising:

a first substrate provided with a plurality of gate and data lines, the gate lines being arranged to cross the data lines to define a plurality of pixel regions in a matrix arrangement;

a second substrate provided with a black matrix layer to shield portions other than the pixel regions from light; and

a liquid crystal display layer injected between the first and second substrates, wherein portions of the black matrix layer corresponding to at least one of a first data line and a last data line among the data lines has a greater width than portions of the black matrix layer corresponding to other data lines.

12. The liquid crystal display panel of claim 11, wherein a portion of the black matrix layer corresponding to a first gate line among the gate lines has a greater width than portions of the black matrix layer corresponding to other gate lines.

13. An liquid crystal display panel comprising:

a plurality of gate lines arranged in one direction at constant intervals;

a plurality of data lines arranged at constant intervals to be substantially perpendicular to the gate lines to define a plurality of pixel regions in a matrix arrangement; and

a plurality of light-shielding patterns in at least one of the pixel regions at a surrounding portion among the pixel regions.

14. The liquid crystal display panel of claim 13, wherein the light-shielding patterns are in the pixel regions adjacent to at least one of the a first data line and a last data line among the data lines.

15. The liquid crystal display panel of claim 13, wherein the light-shielding patterns are metal.

16. The liquid crystal display panel of claim 15, wherein the metal is the same material as that of the data lines.

17. An liquid crystal display panel, comprising:
first and second substrates;

a plurality of gate and data lines arranged on the first substrate to define a plurality of pixel regions, the gate lines crossing the data lines;

a plurality of pixel electrodes, wherein at least one pixel electrode is in each pixel region; and

a black matrix pattern formed on the second substrate to shield portions other than the pixel electrodes from light,

wherein width of a first gate line among the gate lines is greater than widths of other gate lines, width of one of a first data line and a last data line among the data lines is greater

than widths of other data lines, and width of portions the black matrix pattern corresponding to one of the first gate line, the first data line and the last data line is greater than widths of portions of the black matrix pattern not corresponding to the one of the first gate line, the first data line, and the last data line.

18. The liquid crystal display panel of claim 17, further comprising a plurality of thin film transistors in a crossing portion of a respective gate line and a respective data line.

19. The liquid crystal display panel of claim 17, wherein area of the pixel electrodes adjacent to at least one of the first data line and the last data line is less than areas of other pixel electrodes.

20. The liquid crystal display panel of claim 17, wherein aperture ratio the pixel electrodes adjacent to at least one of the first data line and the last data line is about 10-15% lower than aperture ratios of other pixel electrodes.